

# MOS FET Array STA508A

## Absolute Maximum Ratings (Ta=25°C)

Symbol	Ratings	Unit
V <sub>DSS</sub>	120	V
V <sub>GSS</sub>	±20	V
I <sub>D</sub>	±6	A
I <sub>D</sub> (pulse)*1	±10	A
P <sub>T</sub>	4 (Ta = 25°C)	W
	20 (Tc = 25°C)	W
E <sub>AS</sub> *2	80	mJ
T <sub>ch</sub>	150	°C
T <sub>stg</sub>	-55 to +150	°C

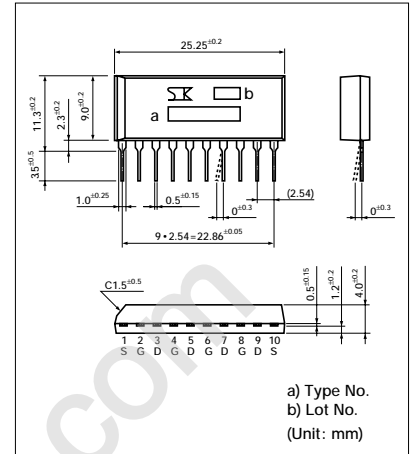
\*1 P<sub>W</sub> ≤ 100μs, duty ≤ 1%

\*2 V<sub>DD</sub> = 12V, L = 10mH, unclamped, R<sub>G</sub> = 50Ω

## Electrical Characteristics (Ta=25°C)

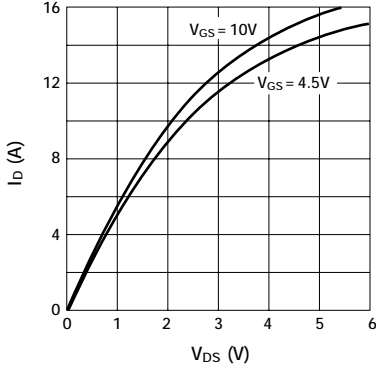
Symbol	Test Conditions	Ratings			Unit
		min	typ	max	
V <sub>(BR)DSS</sub>	I <sub>D</sub> = 100μA, V <sub>GS</sub> = 0V	120			V
I <sub>GSS</sub>	V <sub>GS</sub> = ±20V			±5	μA
I <sub>DSS</sub>	V <sub>DS</sub> = 120V, V <sub>GS</sub> = 0V			100	μA
V <sub>TH</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 250μA	1.0		2.0	V
Re (yfs)	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4.0A	5.0			S
R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.0A		0.15	0.2	Ω
	V <sub>GS</sub> = 4V, I <sub>D</sub> = 4.0A		0.2	0.25	Ω
C <sub>iss</sub>	V <sub>DS</sub> = 10V		400		pF
C <sub>oss</sub>	f = 1.0MHz		130		pF
C <sub>rss</sub>	V <sub>GS</sub> = 0V		30		pF
t <sub>d(on)</sub>	I <sub>D</sub> = 4A		100		ns
t <sub>r</sub>	V <sub>DD</sub> = 12V		300		ns
t <sub>d(off)</sub>	R <sub>L</sub> = 3Ω		250		ns
t <sub>f</sub>	V <sub>GS</sub> = 5V		200		ns
t <sub>f</sub>	R <sub>G</sub> = 50Ω		200		ns
V <sub>SD</sub>	I <sub>SD</sub> = 6A, V <sub>GS</sub> = 0V	1.0	1.5		V

## External Dimensions STA4 (LF412)

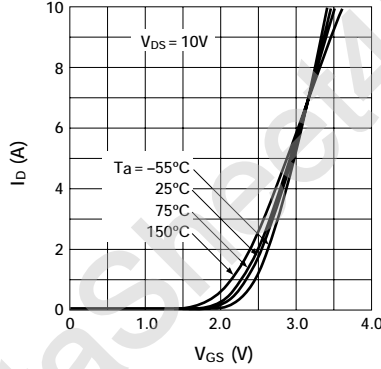


a) Type No.  
b) Lot No.  
(Unit: mm)

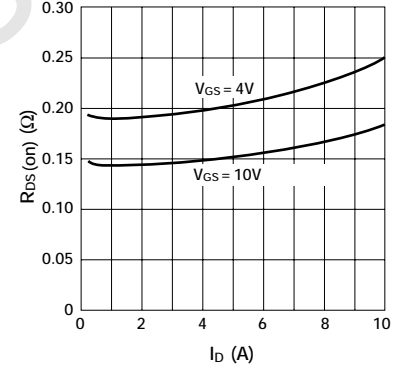
### I<sub>D</sub>—V<sub>DS</sub> Characteristics



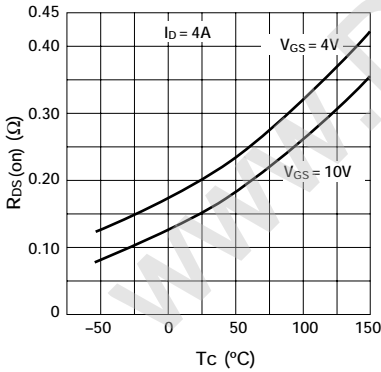
### I<sub>D</sub>—V<sub>GS</sub> Characteristics



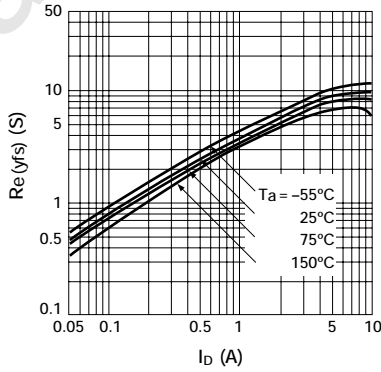
### R<sub>DS(on)</sub>—I<sub>D</sub> Characteristics



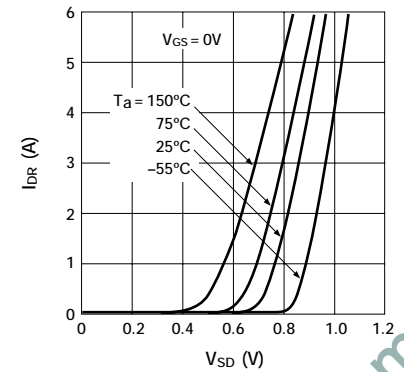
### R<sub>DS(on)</sub>—T<sub>c</sub> Characteristics



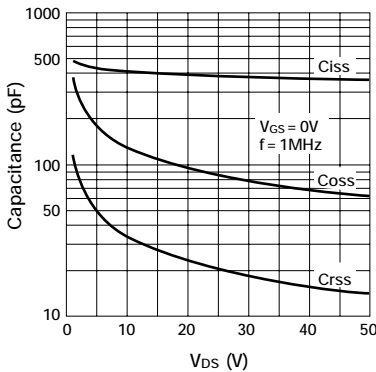
### Re (yfs)—I<sub>D</sub> Characteristics



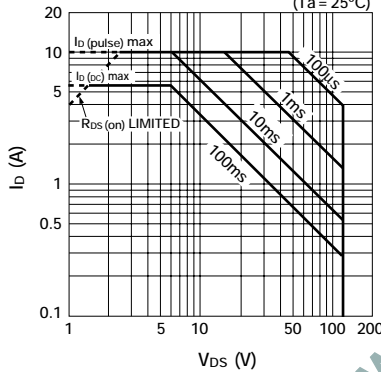
### I<sub>DR</sub>—V<sub>SD</sub> Characteristics



### Capacitance—V<sub>DS</sub> Characteristics



### Safe Operating Area (single pulse)



### Equivalent Circuit Diagram

